

Si PIN photodiodes

S5106/S5107/S7509/S7510 series

Surface mountable, high-speed response Si PIN photodiodes

The S5106, S5107, S7509, and S7510 are Si PIN photodiodes sealed in surface mountable chip carrier packages. They can be mounted using solder reflow, which facilitates automation. Since the photosensitive area is large, they are suitable for FSO (free space optics) and other applications that require a wide field of view. In addition, they can be used in a wide variety of applications including POS, measurements, and analysis.

Features

- Surface mount type ceramic chip carrier package
- Compatible with lead-free solder reflow
- → High sensitivity, high-speed response
- Packing

Tray: S5106, S5107, S7509, S7510

Reel: S5106-10, S5107-10, S7509-10, S7510-10

Applications

- ⇒ FSO
- → Laser radars
- Power meters
- Barcode readers

Structure

Type no.	Photosensitive area (mm)	Package	Window material	
S5106/-10	5 × 5			
S5107/-10	10 × 10	Ceramic	Silicone resin	
S7509/-10	2 × 10	Ceramic	Silicone resili	
S7510/-10	6 × 11			

- Absolute maximum ratings

Type no.	Reverse voltage VR	Power dissipation P	Operating temperature Topr*1	Storage temperature Tstg*1	Soldering temperature Tsol*1
, ·	(V)	(mW)	(°C)	(°C)	(°C)
S5106/-10					
S5107/-10	30	50	-40 to +100	-40 to +125	260 (3 times)* ²
S7509/-10	30	50	-40 10 +100	-40 (0 +125	200 (3 times) -
S7510/-10					

^{*1:} No dew condensation

When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

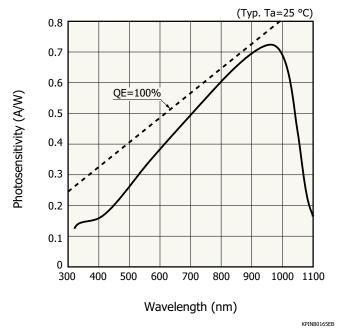
Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

^{*2:} Reflow soldering, JEDEC J-STD-020 MSL 3, see P.9

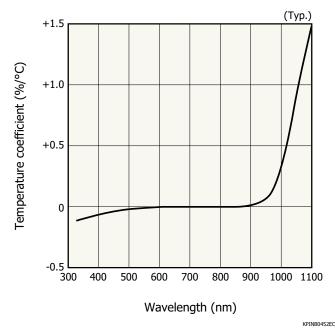
■ Electrical and optical characteristics (Typ. Ta=25 °C, unless otherwise noted)

Type no.	Spectral response range	Peak sensitivity wavelength		9	ensitivi S 'W)	ty	current Isc	I	current D 10 V	Dark current temperature coefficient	Cutoff frequency fc RL=50 Ω	Terminal capacitance Ct f=1 MHz	NEP VR=10 V λ=λp
	λ	λр	١			020	100 lx	Тур.	Max.	TCID	VR=10 V	VR=10 V	Λ-ΛΡ
	(nm)	(nm)	λр	660 nm 780 nm	/80 nm	m 830 nm	(µA)	(nA)	(nA)	(times/°C)	(MHz)	(pF)	(W/Hz ^{1/2})
S5106/-10							27	0.4	5		20	40	1.6×10^{-14}
S5107/-10	320 to 1100	060	0.72	0.45	0.57	0.62	110	0.9	10	1.15	10	150	2.4×10^{-14}
S7509/-10	320 10 1100	0 1100 960	960 0.72	J./2 U. 4 5	0.57 0.62	0.02	22	0.5	5	1.15	20	40	1.7×10^{-14}
S7510/-10							72	1.0	10		15	80	2.5×10^{-14}

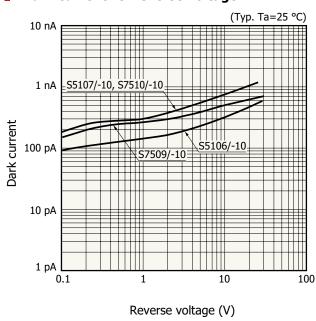
Spectral response



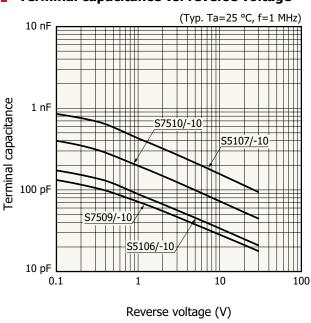
Sensitivity temperature characteristics



Dark current vs. reverse voltage



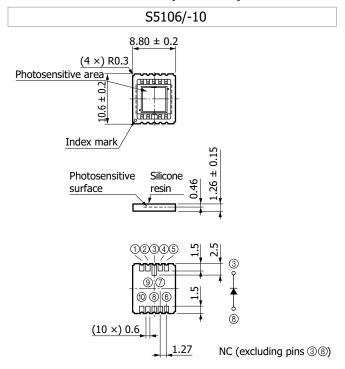
Terminal capacitance vs. reverse voltage



KPINB0128EB

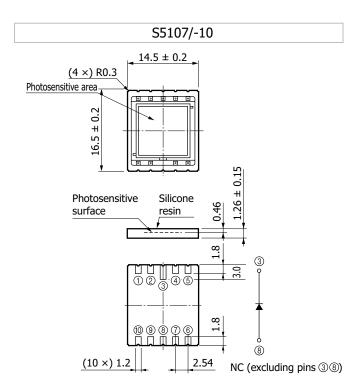


Dimensional outlines (unit: mm)



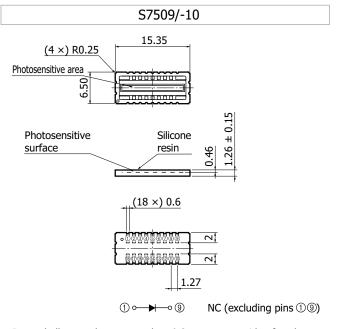
Burrs shall protrude no more than 0.3 mm on any side of package.

KPINA0002EF



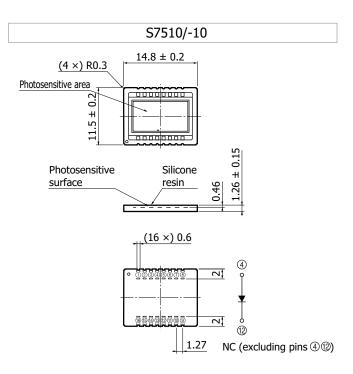
Burrs shall protrude no more than 0.3 mm on any side of package.

KPINA0013ED



Burrs shall protrude no more than 0.3 mm on any side of package.

PINA0055EB



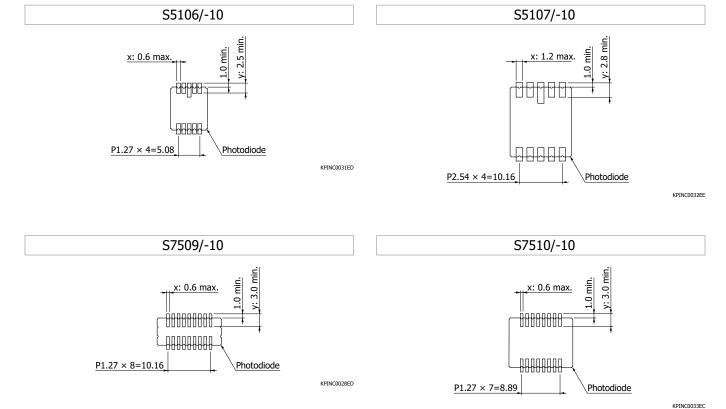
Burrs shall protrude no more than 0.3 mm on any side of package.

KPINA0056EB

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Recommended land patterns (unit: mm)



- 1. Solder all terminals.
- 2. Do not make the land area larger than necessary.
- 3. It is preferable that the land sizes be about equal.
- 4. Make land width x about the same as the terminal width.
- 5. Make land height y at least 1 mm longer than the terminal height, protruding outside the package.

Standard packing specifications

S5106, S5107, S7509, S7510

■ Packing quantity

S5106, S7509: 100 pcs max./tray S5107, S7510: 50 pcs max./tray

■ Packing state

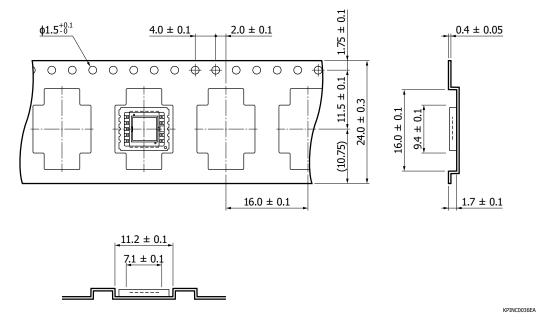
Tray and desiccant in moisture-proof packaging (vacuum-sealed)

S5106-10

■ Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
φ254 mm	ф100 mm	24 mm	PS	Conductive

■ Embossed tape (unit: mm, material: PS, conductive)



- Packing quantity 1000 pcs/reel
- Packing state

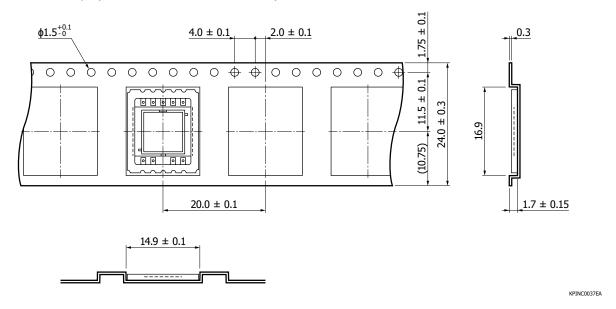
 Reel and desiccant in moisture-proof packaging (vacuum-sealed)

S51	Λ7	_1	n
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■ Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
ф330 mm	ф80 mm	24 mm	PS	Conductive

■ Embossed tape (unit: mm, material: PS, conductive)



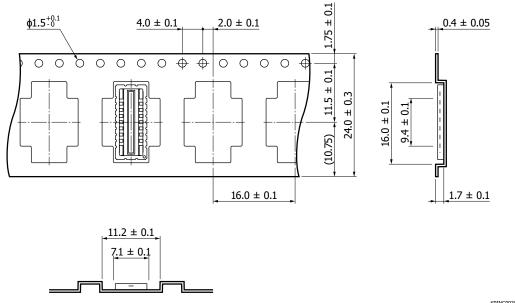
- Packing quantity 100 pcs/reel
- Packing state
 Reel and desiccant in moisture-proof packaging (vacuum-sealed)

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■ Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
φ254 mm	ф100 mm	24 mm	PS	Conductive

■ Embossed tape (unit: mm, material: PS, conductive)



KPINC0038EA

- Packing quantity 1000 pcs/reel
- Packing state

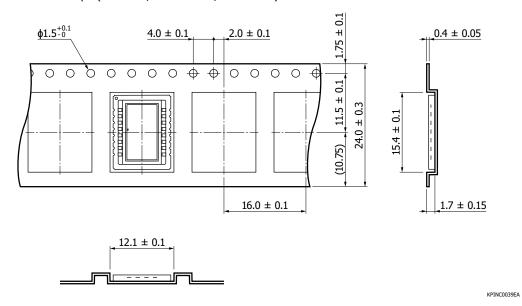
 Reel and desiccant in moisture-proof packaging (vacuum-sealed)

S7	5	1	Λ	_1	Λ
<i>3</i> /	J	1	v	_ 1	.v

■ Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
ф254 mm	ф100 mm	24 mm	PS	Conductive

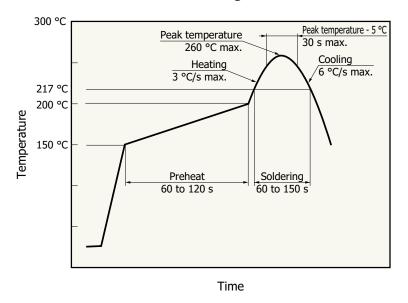
■ Embossed tape (unit: mm, material: PS, conductive)



- Packing quantity100 pcs/reel
- Packing state

 Reel and desiccant in moisture-proof packaging (vacuum-sealed)

Recommended reflow soldering conditions



- · After unpacking, store in an environment at a temperature of 30 °C or less and a humidity 60% or less, and perform reflow soldering within 168 hours.
- · The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. When you set reflow soldering conditions, check that problems do not occur in the product by testing out the conditions in advance.

KMPDB0405EC

Baking

If more than 12 months have passed in the unopened state, or storage conditions are exceeded after opening the package, baking is required to remove moisture before reflow soldering. For the baking, refer to "Precautions / Surface mount type products" in the related information.

■ Recommended baking conditions

Temperature: 150 °C (3 to 5 hours) or 120 °C (12 to 15 hours)

Note: Before setting the baking conditions, perform experiments to confirm that no problems occur with the product.

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- · Disclaimer
- · Precautions / Surface mount type products
- Catalogs
- Technical note / Si photodiodes

Information described in this material is current as of December 2024.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

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