

CCD multichannel detector heads



C7020/C7021/C7025 series

Designed for front-illuminated CCD area image sensors

The C7020/C7021/C7025 series are high sensitivity multichannel detector heads intended for use with front-illuminated CCD area image sensors (S9970/S9971/S9972/S9973 series). The C7020 series is designed for non-cooled CCD, while the C7021/C7025 series for thermoelectric cooled CCD. All of the C7020/C7021/C7025 series contain a low-noise driver circuit that operates on input of simple external signals. Mounting holes are provided on the front panel for attaching to other devices such as spectrometers.

The C7021/C7025 series also include a highly stable temperature control circuit that cools the sensor to a preset temperature ($T_{chip} = -10\text{ }^{\circ}\text{C}$) as soon as the power is turned on. If the cooler fails and causes overheating, the internal protection circuit automatically turns off the power to ensure safety. Despite the compact size, the housing configuration is designed for good heat dissipation.

As an option, HAMAMATSU also provides the C7557-01 controller for multichannel detector heads. Using the dedicated software for the C7557-01 allows easy control and data acquisition of a multichannel detector head from a PC.

Features

- **Designed for front-illuminated CCD area image sensor*1**
C7020/-02: for non-cooled type
C7021/-02, C7025/-02: for TE-cooled types
- **Line binning operation*2/area scanning operation**
- **Driver circuit for low noise CCD operation**
- **Highly stable temperature controller (C7021/-02, C7025/-02)**
- **Simple signal input operation**
- **Compact configuration**

Applications

- **Fluorescence spectroscopy**
- **Raman spectroscopy**
- **Other low light level detection**

Selection guide

The table below shows CCD area image sensors applicable for C7020/C7021/C7025 series.

Since C7020/C7021/C7025 series do not include a CCD area image sensor, so select the desired sensor and order it separately.

Type No.	CCD area image sensor			
	Type no.	Number of pixels	Number of effective pixels	Image area [mm (H) × mm (V)]
C7020	S9970-0906	532 × 64	512 × 60	12.288 × 1.440
	S9970-1006	1044 × 64	1024 × 60	24.576 × 1.440
	S9970-1007	1044 × 128	1024 × 124	24.576 × 2.976
	S9970-1008	1044 × 256	1024 × 252	24.576 × 6.048
C7020-02	S9972-1007	1044 × 128	1024 × 124	24.576 × 2.976
	S9972-1008	1044 × 256	1024 × 252	24.576 × 6.048
C7021	S9971-0906	532 × 64	512 × 60	12.288 × 1.440
	S9971-1006	1044 × 64	1024 × 60	24.576 × 1.440
	S9971-1007	1044 × 128	1024 × 124	24.576 × 2.976
C7021-02	S9973-1007	1044 × 128	1024 × 124	24.576 × 2.976
C7025	S9971-1008	1044 × 256	1024 × 252	24.576 × 6.048
C7025-02	S9973-1008	1044 × 256	1024 × 252	24.576 × 6.048

*1: The FFT-CCD (full frame transfer CCD) has charge transfer sections that are also used as light receiving areas, being different from interline transfer CCD (IT-CCD) commonly used in video cameras. Compared to the IT-CCD, the FFT-CCD offers advantages of low dark current, a 100 % open area ratio and low image lag.

*2: The FFT-CCD area image sensor can be operated like a linear image sensor having a large active area by transferring all the pixel signals in the vertical direction to the horizontal register (this is referred to as line binning).

➤ Absolute maximum ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Supply voltage (for digital circuitry)	VD1	-0.5	-	+7	V
Supply voltage (for analog circuitry)	VA1+	-	-	+18	
	VA1-	-	-	-18	
	VA2	-	-	+30	
Supply voltage*3	VD2	-	-	+7	
	Vp	-	-	+7	
	VF	-	-	+14	
Digital input voltage	-	-	-	Vd (1, 2)	
Operating temperature	C7020/-02	0	-	+50	°C
	C7021/-02, C7025/-02	+10	-	+35 *3	
Storage temperature	C7020/-02	-20	-	+70	
	C7021/-02, C7025/-02	0	-	+50	

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

➤ Electrical characteristics

(Ta=25 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V, unless otherwise noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit	
Digital input	High level	VIH	+2.0	-	VD (1, 2)	
	Low level	VIL	-0.5	-	+0.8	
CLK frequency	fclk	-	-	1	MHz	
Data video readout frequency	fv	-	-	fCLK/4	Hz	
Start pulse width	tst	1/fCLK	-	-	s	
Digital output	High level (Io=-6 mA)	VIH	+2.0	-	-	
	Low level (Io=+6 mA)	VIL	-	-	+0.8	
Power supply operating conditions						
Voltage	Digital circuitry	VD1	+4.75	+5.0	+5.25	V
	Analog circuitry	VA1+	+14.5	+15.0	+15.5	V
		VA1-	-14.5	-15.0	-15.5	V
		VA2	+23.5	+24.0	+24.5	V
	Other*4	VD2	+4.75	+5.0	+5.25	V
		Vp	+4.75	+5.0	+5.25	V
VF		+11.75	+12.0	+12.75	V	
Current	VD1 (+5 V)	-	-	-	+200	mA
	VA1+ (+15 V)	-	-	-	+100	mA
	VA1- (-15 V)	-	-	-	-100	mA
	VA2 (+24 V)	-	-	-	+30	mA
	VD2 (+5 V)*4	-	-	-	+30	mA
	Vp (+5 V)*4	-	-	-	+2.5	A
	VF (+12 V)*4	-	-	+100	-	mA

➤ Electrical and optical characteristics

(Ta=25 °C, Tchip= -10 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V, unless otherwise noted)

Parameter	Symbol	C7020, C7021, C7025			C7020-02, C7021-02, C7025-02			Unit
		Min.	Typ.	Max.	Min.	Typ.	Max.	
Spectral response range	λ	-	400 to 1100	-	-	400 to 1100	-	nm
Full well capacity	Vertical	150	300	-	120	240	-	ke ⁻
	Horizontal	300	600	-	240	480	-	
Conversion gain*5	Sv	-	15	-	-	12	-	μV/e ⁻
Dark current*6	C7020/-02	-	200	3000	-	2000	30000	e ⁻ /pixel/s
	C7021/-02, C7025/-02	-	3	45	-	30	450	
Readout noise	Nread	-	20	45	-	60	120	e ⁻ rms
Dynamic range	Line binning	-	30000	-	-	8000	-	-
	Area binning	-	15000	-	-	4000	-	-
Photoresponse nonuniformity*7	PRNU	-	-	±10	-	-	±10	%

*3: The maximum value may be lower, depending on the drive frequency and the number of sensor pixels.

*4: C7021/C7025 series

*5: Including the circuit gain

*6: At MPP mode. Vertical register value. The actual value equals the sum of the vertical direction because of the binning operation.

*7: Measured at 50 % of the full well capacity

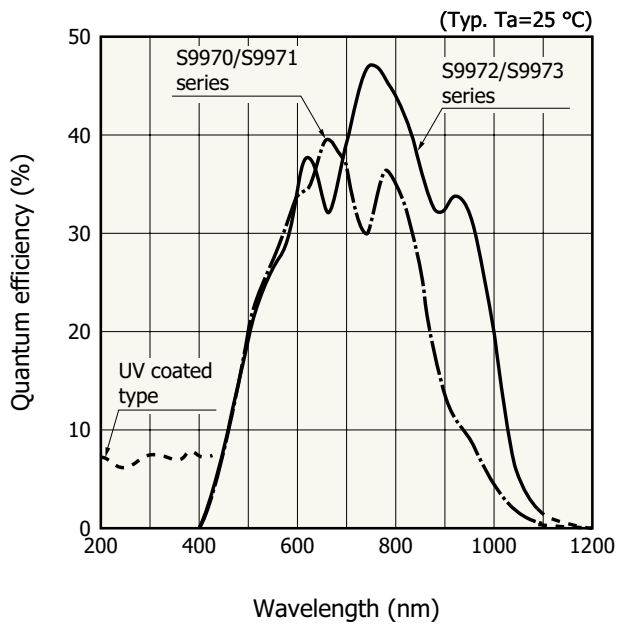
Specifications for temperature controller (C7021/-02, C7025/-02)

($T_a=25\text{ }^\circ\text{C}$, $V_{D1}=+5\text{ V}$, $V_{A1+}=+15\text{ V}$, $V_{A1-}=-15\text{ V}$, $V_{A2}=+24\text{ V}$, $V_{D2}=+5\text{ V}$, $V_p=+5\text{ V}$, $V_F=+12\text{ V}$)

Parameter**8	Symbol	Min.	Typ.	Max.	Unit
Cooling temperature	T_s	-11.5	-10	-8.5	$^\circ\text{C}$
Temperature control range	ΔT_s	-0.05	-	+0.05	$^\circ\text{C}$
Power dissipation of TE-cooler element	P_p	-	-	7	W
Cool down time to reset temperature	t_o	-	-	5	min
Setting temperature for overheat protection	T_o	-	+45	-	$^\circ\text{C}$

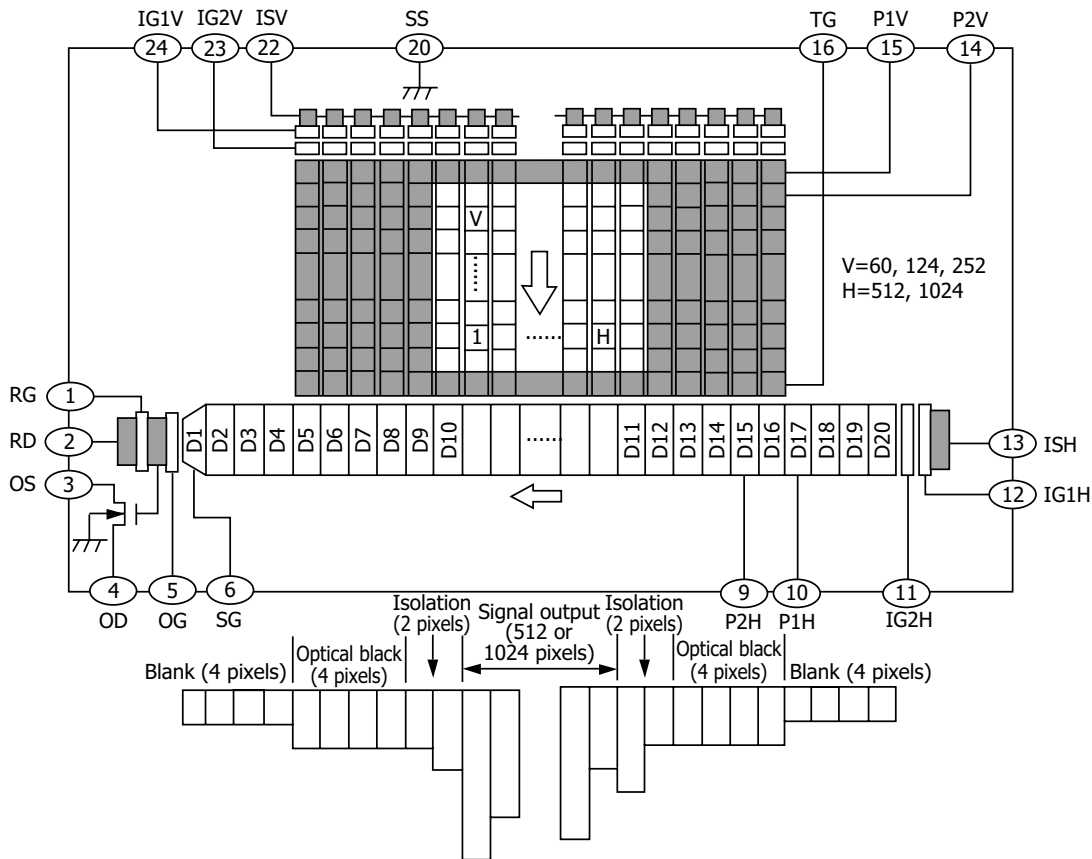
*8: Other functions include error display, automatic power off, and detection of electrical opens and shorts by the thermosensor.

Spectral response (without window)



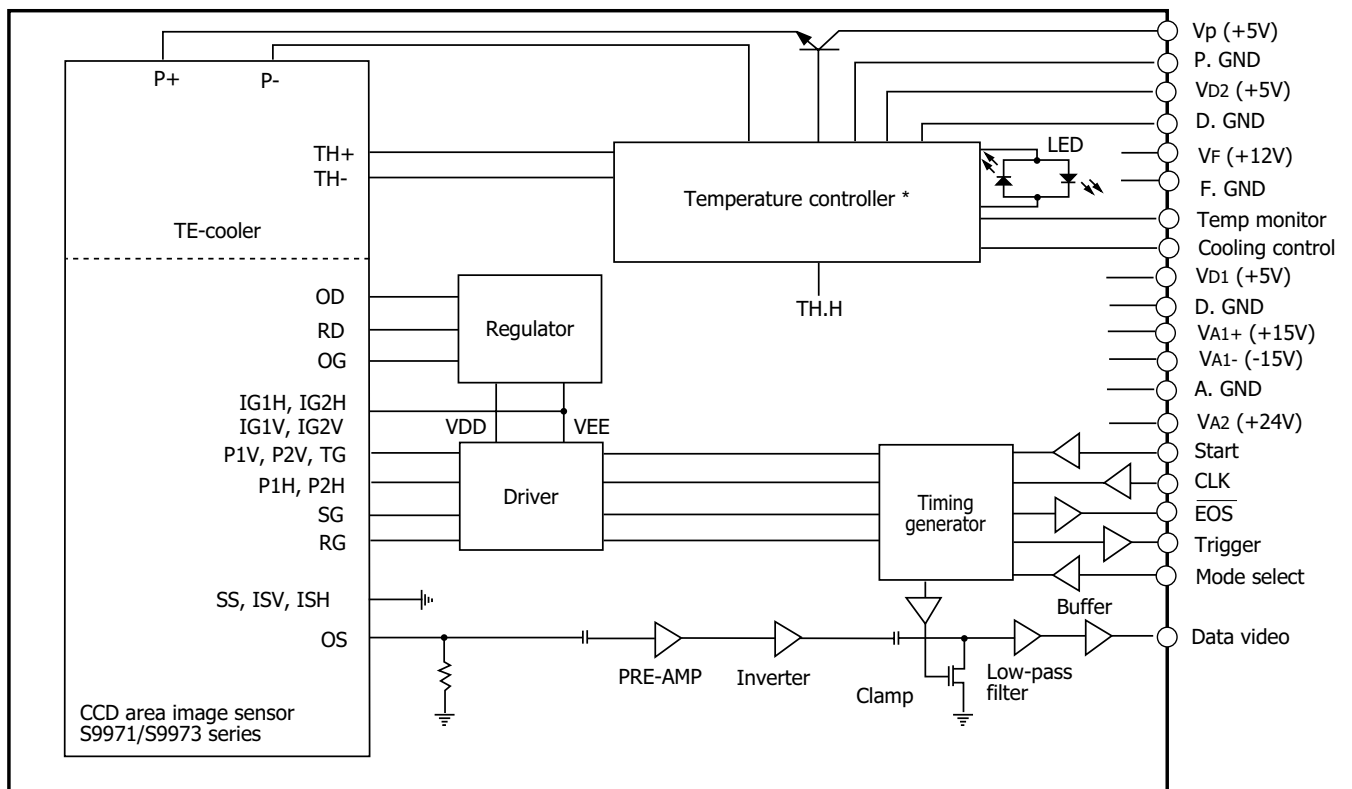
KMPD80257ED

Device structure (front-illuminated CCD area image sensor)



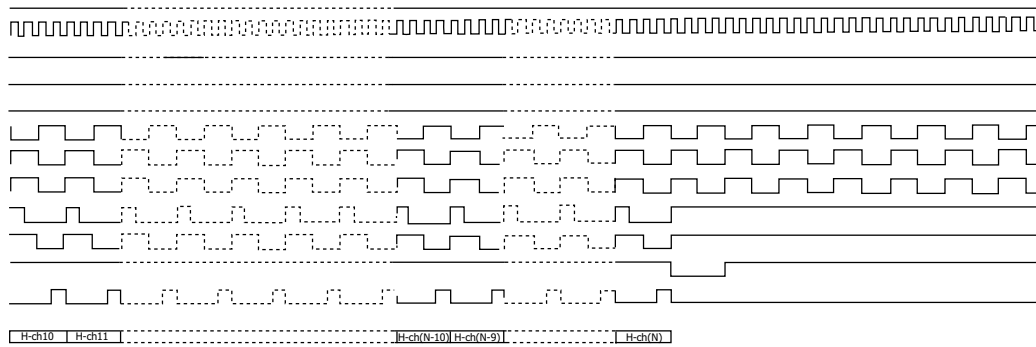
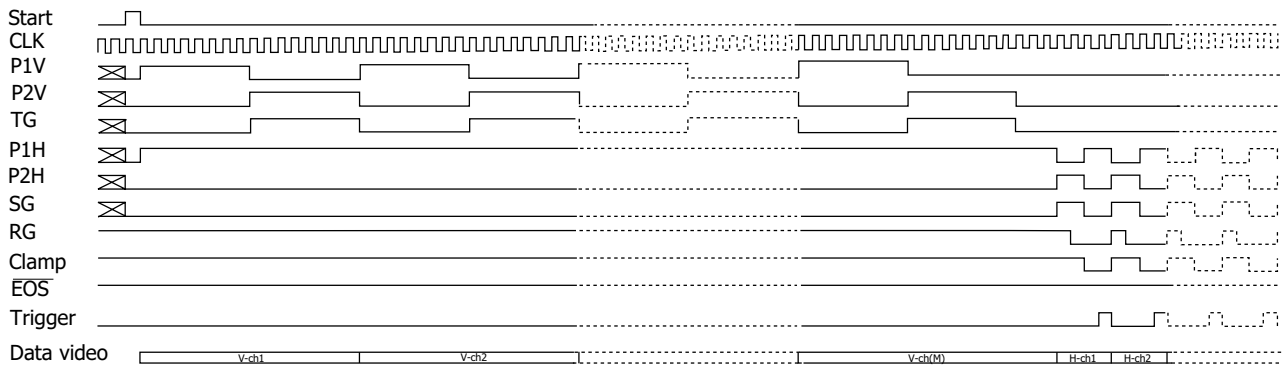
KMPDC0015EB

Block diagram (C7021 series, C7025 series)



* The C7020 series does not include the temperature controller and TE-cooler.

Timing chart

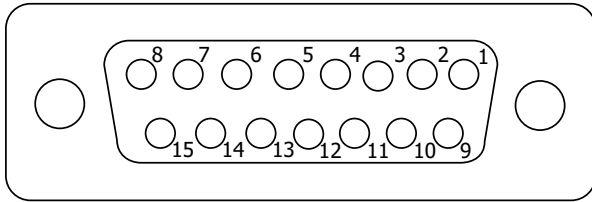


M=64, 128, 256 N=532, 1044
 V-ch1--V-ch2, V-ch (M-3)--V-ch (M): Isolation pixels
 H-ch1--H-ch4, H-ch (N-3)--H-ch (N): Blank pixels
 H-ch5--H-ch10, H-ch (N-9)--H-ch (N-4): Isolation pixels

KACCC0457EA

Pin assignment of "SIGNAL I/O" connector

15-pin D-sub connector

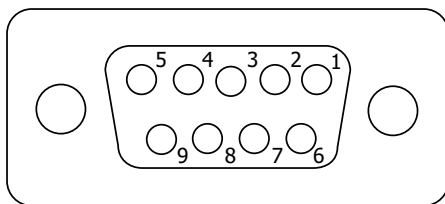


KACCC0069EA

Pin no.	Terminal name	Description
1	Mode select	Digital input signal used to select between the line binning operation and area scanning operation HCMOS compatible High level or left open: line binning operation Low level: area scanning operation
2	Data video	Analog video output. Positive polarity
3	VA1+ (+15 V)	Analog power supply
4	VA1- (-15 V)	Analog power supply
5	VD1 (+5 V)	Digital power supply
6	Start	Digital input signal for initializing the circuit. HCMOS compatible. Positive logic. The interval of the start pulses determines the integration time of the CCD image sensor.
7	CLK	Digital input signal for operating the circuit. HCMOS compatible. Rising edge operation.
8	$\overline{\text{EOS}}$	Digital input signal for indicating end-of-scan of the image sensor. HCMOS compatible. Negative logic.
9	A. GND	Analog ground
10	A. GND	Analog ground
11	VA2 (+24 V)	Analog power supply
12	D. GND	Digital ground
13	D. GND	Digital ground
14	D. GND	Digital ground
15	Trigger	Digital output signal for A/D conversion. HCMOS compatible. Positive logic.

Pin assignment of "TE CONTROL I/O" connector (C7021/-02, C7025/-02)

9-pin D-sub connector



KACCC0075EA

Pin no.	Terminal name	Description
1	VD2 (+5 V)	Digital power supply
2	Temp monitor	Analog output signal of the temperature of the CCD image sensor
3	Cooling control	Digital input signal for starting to cool down. HCMOS compatible. High level or left open: cooling Low level: stand-by
4	Vp (+5 V)	Power supply for the thermoelectric cooler in the CCD image sensor (Please use AWG 18 wire.)
5	Vf (+12 V)	Power supply for cooling fan
6	D. GND	Ground
7	D. GND	Ground
8	P. GND	Power supply return of the thermoelectric cooler mounted in the CCD image sensor (Please use AWG 18 wire.)
9	F. GND	Power supply return for cooling fan

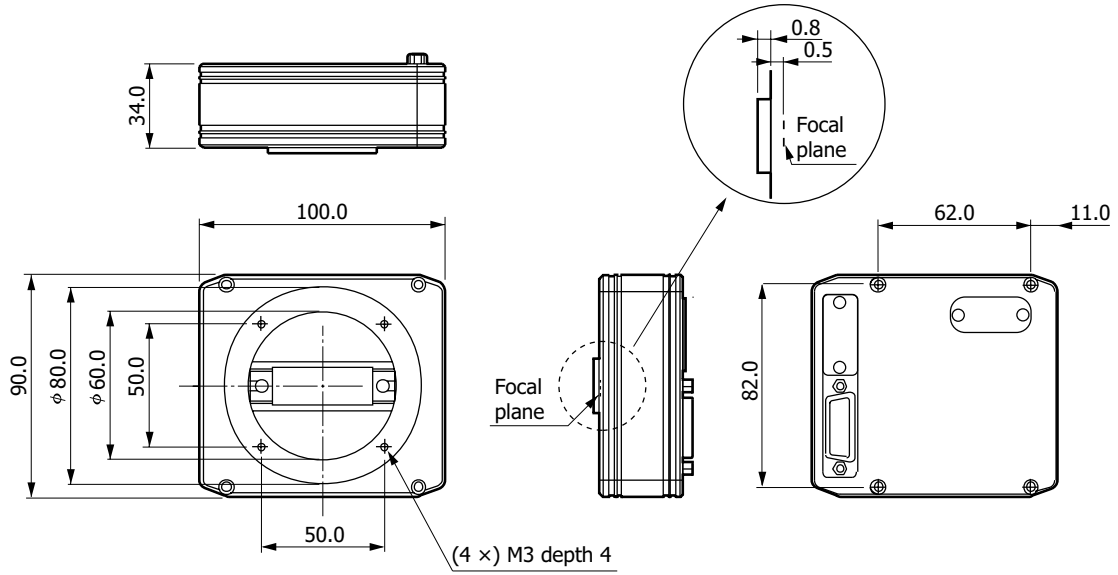
Available for using same power supply (+5 V) for "+VD2" and "+Vp".

Caution: Do not connect "VD2" and "Vp" together on the backside of the 9-pin D-sub connector.

These may be connected (shorted) at the power supply end, not 9-pin D-sub connector.

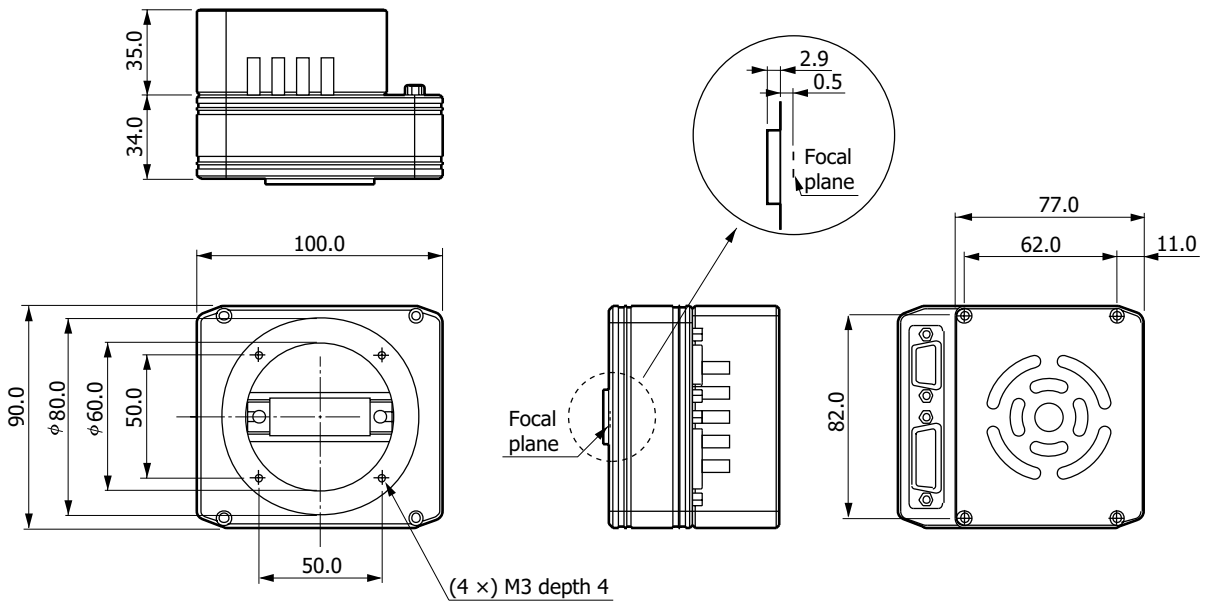
Dimensional outlines (unit: mm)

C7020/-02



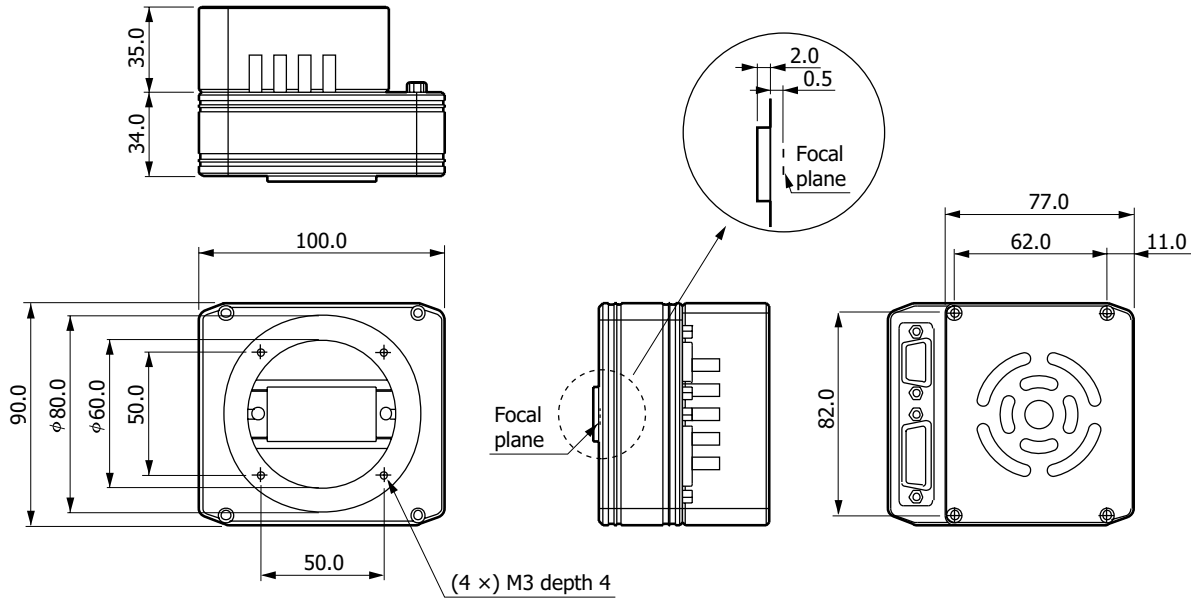
KACCA0058EB

C7021/-02



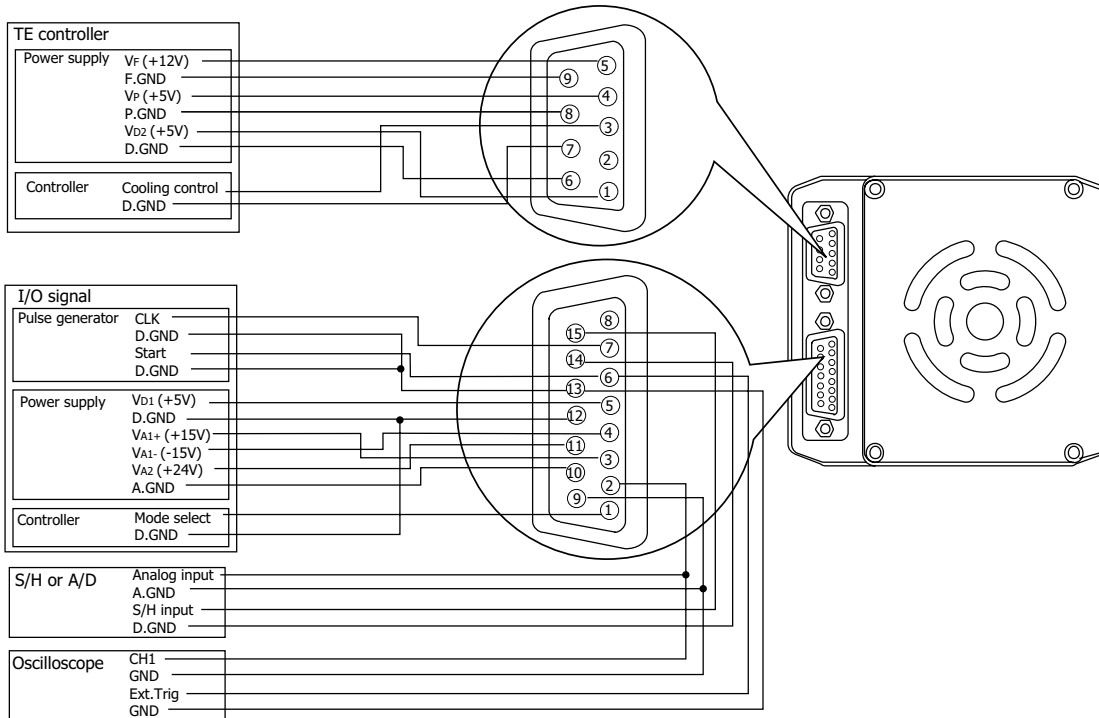
KACCA0059EC

C7025/-02



KACCA0060EC

Pin connections



KACCC0076EC

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- Disclaimer

Multichannel detector head controller C7557-01

The C7557-01 is specifically designed for basic control in multichannel photometry. When connected to a HAMAMATSU multichannel detector head and PC, the C7557-01 allows easy control of the detector head and data acquisition by using dedicated software that comes with the unit.

Accessories for C7557-01

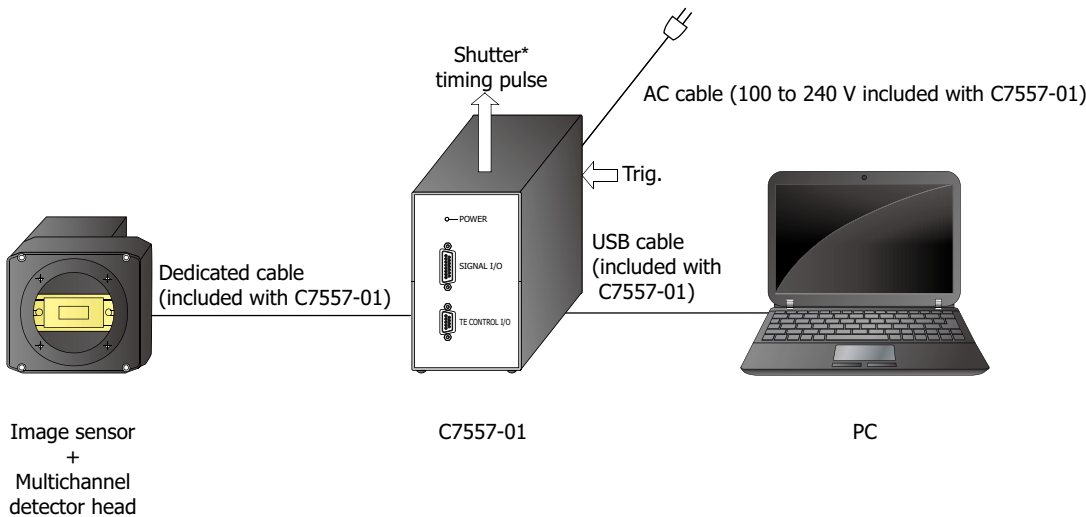
- Spare fuse (2.5 A)*9
- AC cable
- 2 to 3 conversion adapter
- USB cable
- Detector head connection cables (for "SIGNAL I/O" and "TE CONTROL I/O" terminal of multichannel detector head)
- CD-R (MCD USB driver, Software, Operation manual)
- MOS adapter*10

*9: Contained in the holder just above the AC cable connector on the C7557-01 rear panel.

*10: For connection of NMOS multichannel detector head. Do not use for CCD multichannel detector head. It can cause abnormal operation.



Connection example



Note:
 * Shutter, etc. are not available.
 * For supported OS, see the datasheet of C7557-01.

KACCC0402EE

Information described in this material is current as of June 2021.

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