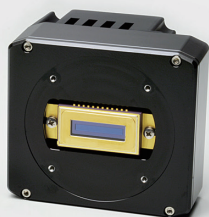


Image sensor module

C7044A



Module with built-in back-thinned CCD image sensor

The C7044A is an image sensor module with a built-in back-thinned CCD area image sensor (S7034-1007S) that offers large saturation charge. A low-noise driver circuit is built in, allowing operation by applying simple external signals.

C7044A has a built-in highly stable temperature control circuit, which cools the sensor to the predetermined temperature ($T_{chip} = -10\text{ }^{\circ}\text{C}$) as soon as the power is supplied. It is designed with a safety feature that automatically turns off the power via a protective circuit in the event of overheating caused by a cooler failure. The main unit has a highly effective heat dissipation structure despite its compact exterior, and is equipped with mounting holes on its front surface for attaching a spectrometer or other devices.

A controller (C7557-01) for operating C7044A is also available separately. The dedicated software allows you to control the module and collect data from it via the PC.

Features

- Built-in back-thinned CCD area image sensor^{*1}
- Selectable line binning^{*2} / area scanning operation
- Built-in dedicated driver circuit
- Highly stable temperature control method adopted
- Operation via simple signal inputs
- Compact exterior

*1: In a conventional front-illuminated CCD, the photosensitive area is covered with transparent electrodes for charge transfer. These transparent electrodes block ultraviolet light, which reduces the quantum efficiency. In contrast, a back-thinned CCD is structured such that the backside of the photosensitive area is thinned and light enters from the backside. Therefore, it is not affected by the transparent electrodes, achieving high UV sensitivity and quantum efficiency.

*2: By adding all vertically transferred charges to the corresponding horizontal register (line binning), it can operate like a linear image sensor with a large photosensitive area.

Applications

- Fluorescence spectroscopy
- Raman spectroscopy
- Other low-light-level detection

Selection guide

The following CCD area image sensors are built in.

The circuit is also available for purchase separately. Contact us for detailed information.

Type no.	Built-in CCD area image sensors				
	Type no.	Cooling	Total number of pixels	Number of effective pixels	Photosensitive area [mm (H) × mm (V)]
C7044A	S7034-1007S	TE-cooled type	1044 × 128	1024 × 122	24.576 × 2.928

Absolute maximum ratings

Parameter	Symbol	Min.	Typ.	Max.	Unit
Supply voltage (digital)	VD1, VD2	-0.5	-	+7	V
Supply voltage (analog)	VA1+	-	-	+18	
	VA1-	-	-	-18	
	VA2	-	-	+30	
Supply voltage	Vp	-	-	+7	
	VF	-	-	+14	
Digital input voltage	-	-	-	VD (1, 2)	°C
Operating temperature*3	C7044A	Topr	+10	+30	
Storage temperature*3	C7044A	Tstg	0	+50	

*3: No dew condensation

When there is a temperature difference between a product and the surrounding area in high-humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

Electrical characteristics

(Ta=25 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V, unless otherwise noted)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Digital input	High level	VIH	+2.0	-	VD (1, 2)
	Low level	VIL	-0.5	-	+0.8
CLK frequency	fCLK	-	-	1	MHz
Video signal readout frequency	fv	-	-	fCLK/4	Hz
Start pulse width	tst	1/fCLK	-	-	s
Digital output	High level (Io=-6 mA)	VIH	+2.0	-	V
	Low level (Io=+6 mA)	VIL	-	-	+0.8
Operating power supply conditions					
Voltage	Digital	VD	+4.75	+5.0	+5.25
	Analog	VA1+	+14.75	+15.0	+15.25
		VA1-	-14.75	-15.0	-15.25
		VA2	+23.75	+24.0	+24.25
	Others	VD2	+4.75	+5.0	+5.25
		Vp	+4.75	+5.0	+5.25
		VF	+11.75	+12.0	+12.25
Current	VD1 (+5 VDC)	-	-	-	+200
	VA1+ (+15 VDC)	-	-	-	+100
	VA1- (-15 VDC)	-	-	-	-100
	VA2 (+24 VDC)	-	-	-	+30
	VD2 (+5 VDC)	-	-	-	+30
	Vp (+5 VDC)	-	-	-	+2.5
	VF (+12 VDC)	-	-	-	+150

Electrical and optical characteristics

(Ta=25 °C, Tchip=-10 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Spectral response range	SR	-	200 to 1100	-	nm
Saturation charge	Vertical	Fw	180	360	ke ⁻
	Horizontal		1700	3400	
Conversion gain*4	Sv	-	3	-	μV/e ⁻
Saturation output voltage	Vsat	10	-	-	V
Dark current*5	DS	-	3	30	e ⁻ /pixel/s
Readout noise	Nread	-	60	-	e ⁻ rms
Dynamic range	Drange	-	56000	-	-
Photoresponse nonuniformity*6	PRNU	-	±3	±10	%

*4: Including circuit gain.

*5: In MPP mode. Due to the binning operation, the actual value is the sum of the vertical pixels.

*6: Measured at half of the saturation charge.

Temperature controller specifications

($T_a=25\text{ }^{\circ}\text{C}$, $V_{D1}=+5\text{ V}$, $V_{A1+}=+15\text{ V}$, $V_{A1-}=-15\text{ V}$, $V_{A2}=+24\text{ V}$, $V_{D2}=+5\text{ V}$, $V_p=+5\text{ V}$, $V_f=+12\text{ V}$)

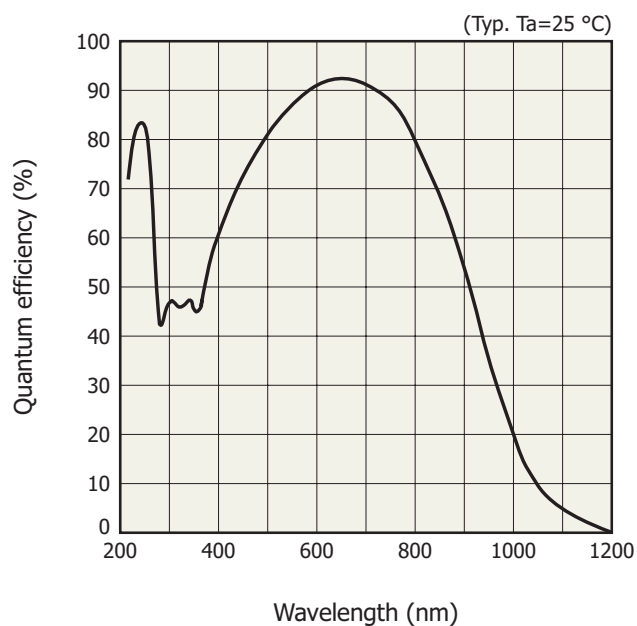
Parameter ^{*7}	Symbol	Min.	Typ.	Max.	Unit
Cooling temperature	T_s	-11.5	-10	-8.5	$^{\circ}\text{C}$
Control temperature range	ΔT_s	-0.05	-	+0.05	$^{\circ}\text{C}$
TE-cooler power dissipation	P_p	-	-	7	W
Setting temperature achievement time	t_o	-	-	5	minutes
Sensor overheat protection set temperature ^{*8}	T_o	-	+45	-	$^{\circ}\text{C}$

*7: In addition, the following functions are included:

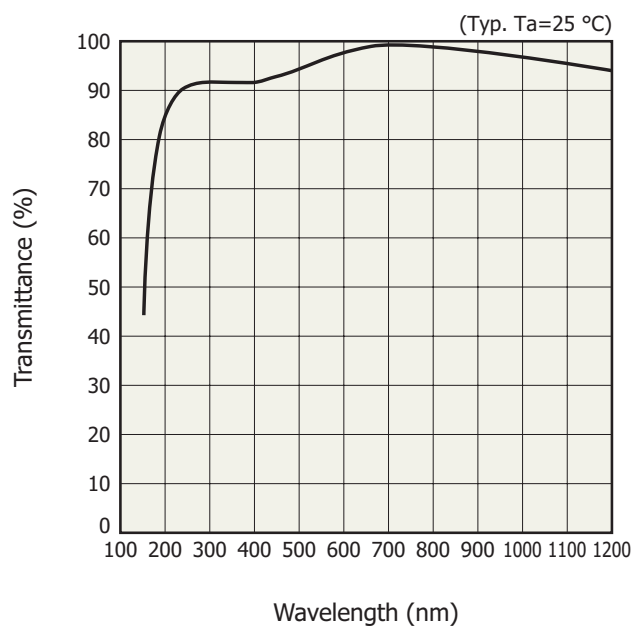
Error display, automatic power-off, detection of open or short in the temperature sensor

*8: Temperature at the back of the case (fan mounting area)

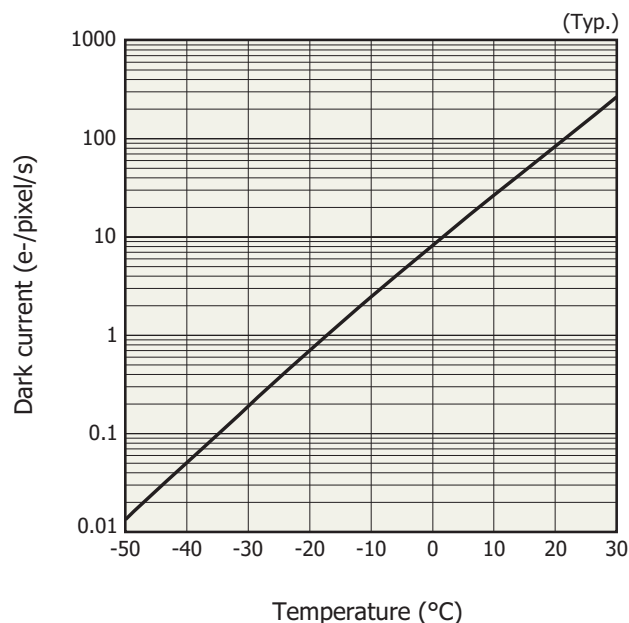
Spectral response (without window, typical example)



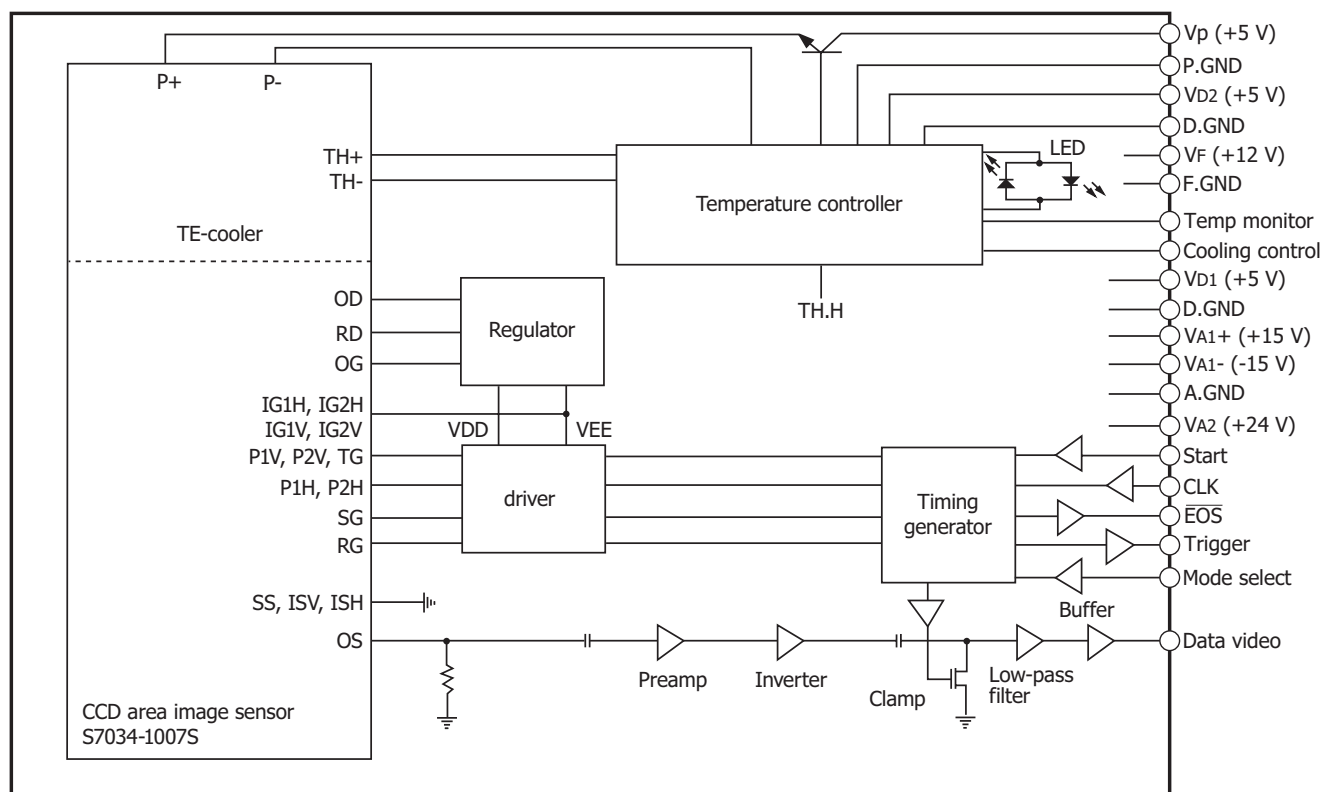
Spectral transmittance characteristics of window material



Dark current vs. temperature

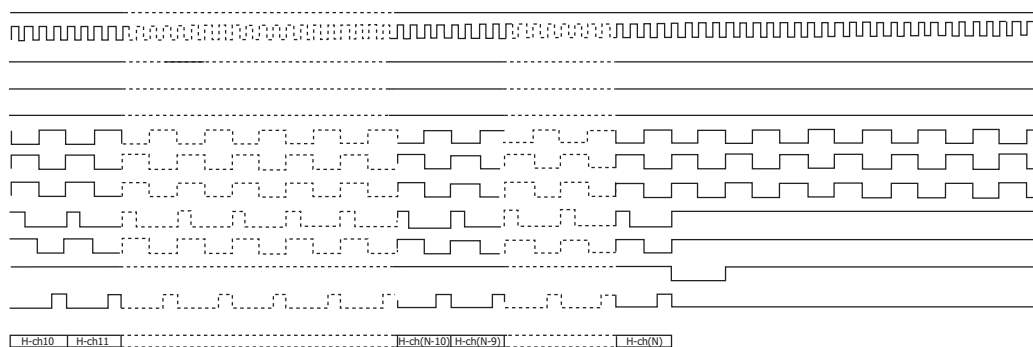
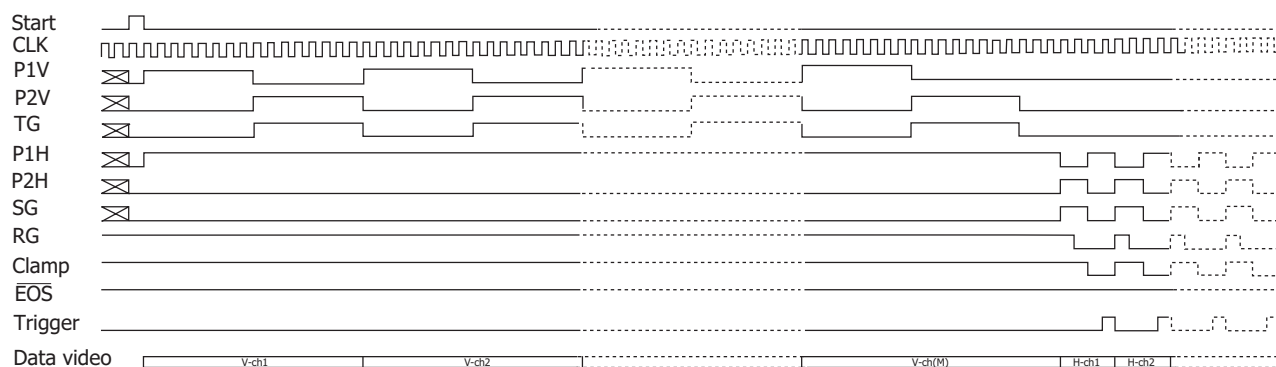


Block diagram



KACCC1268EA

Timing chart (line binning operation)



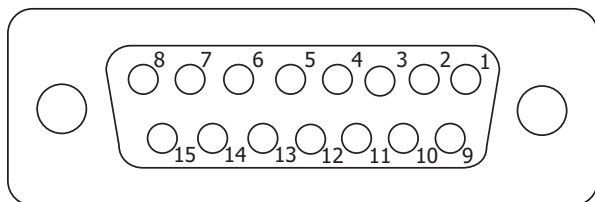
H-ch10 H-ch11 H-ch(N-10) H-ch(N-9) H-ch(N)

M=128 N=1044
 V-ch1--V-ch2, V-ch (M-3)--V-ch (M): Bevel pixels
 H-ch1--H-ch4, H-ch (N-3)--H-ch (N): Blank pixels
 H-ch5--H-ch10, H-ch (N-9)--H-ch (N-4): Bevel pixels

KACCC1271EA

Signal I/O connector pin connections

15-pin D-sub connector

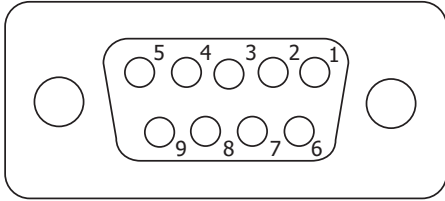


KACCC0069EA

Pin no.	Terminal name	Description
1	Mode select	Digital input signal for switching between binning and area scanning operations HCMOS compatible High level or open: Binning operation Low level: Area scanning operation
2	Data video	Analog video output signal, positive polarity
3	VA1+ (+15 V)	Analog power supply
4	VA1- (-15 V)	Analog power supply
5	VD1 (+5 V)	Digital power supply
6	Start	Digital input signal for circuit initialization HCMOS compatible, positive logic This pulse interval corresponds to the sensor's integration time.
7	CLK	Digital input signal for defining circuit operation HCMOS compatible, operates on the rising edge
8	EOS	Digital output signal indicating the end of CCD image sensor sweep HCMOS compatible, negative logic
9	A.GND	Analog ground
10	A.GND	Analog ground
11	VA2 (+24 V)	Analog power supply
12	D.GND	Digital ground
13	D.GND	Digital ground
14	D.GND	Digital ground
15	Trigger	Digital output signal for A/D conversion, HCMOS compatible, positive logic

TE-cooling controller I/O connector pin connections

9-pin D-sub connector



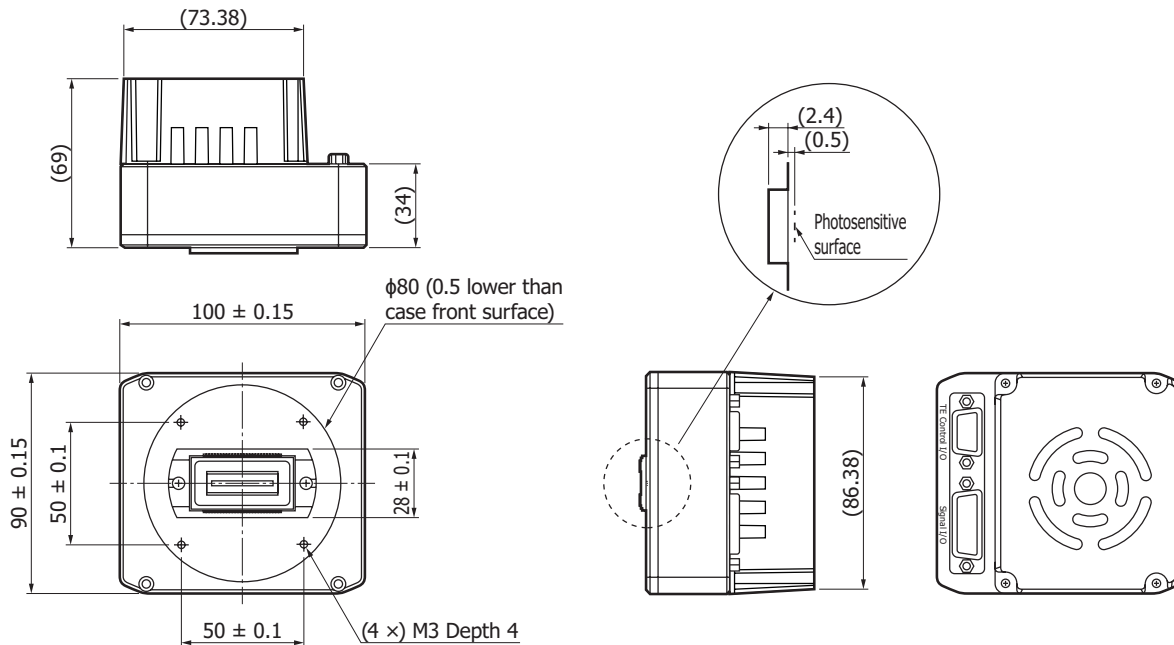
KACCC0075EA

Pin no.	Terminal name	Description
1	V _{D2} (+5 V)	Digital power supply
2	Temp monitor	Analog signal output of the CCD image sensor temperature
3	Cooling control	Digital input signal for cooling start, HCMOS compatible High level or open: Cooling Low level: Standby
4	V _p (+5 V)	Power supply for the TE-cooler inside the CCD image sensor
5	V _F (+12 V)	Power supply for cooling fan
6	D.GND	Ground
7	D.GND	Ground
8	P.GND	Power supply return for the TE-cooler inside the CCD image sensor
9	F.GND	Power supply return for the cooling fan

"V_{D2}" and "V_p" can be used with the same power supply (+5 V).

Warning: Do not connect "V_{D2}" and "V_p" on the back of the 9-pin D-sub connector. Shorting them on the power supply side is acceptable.

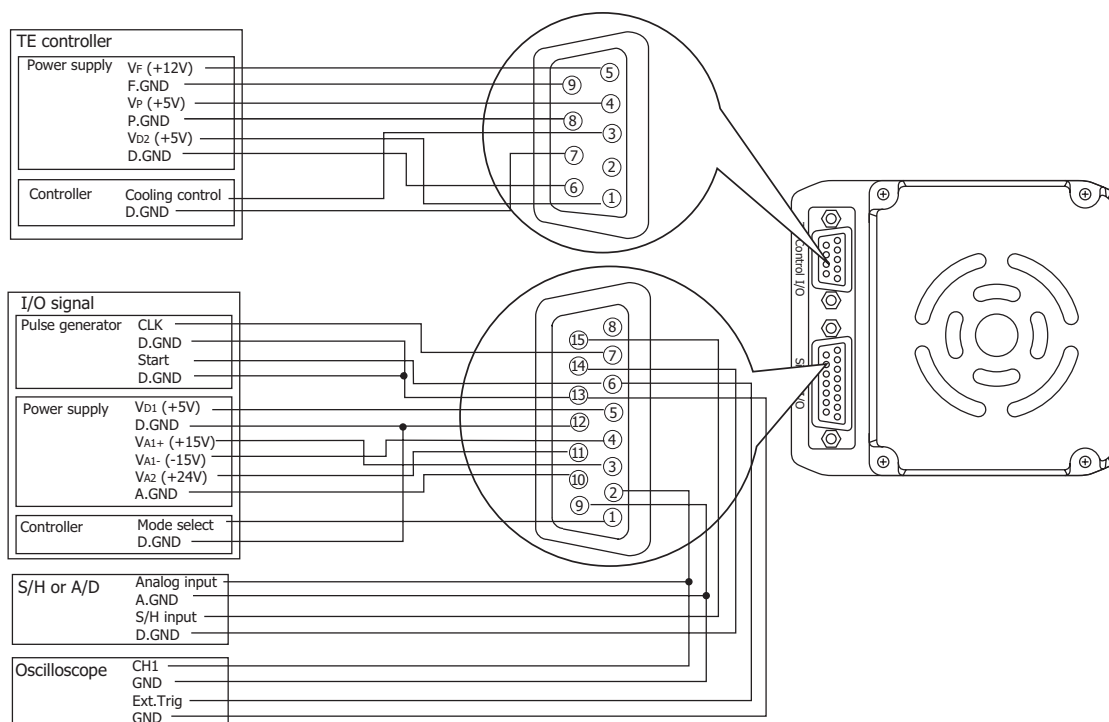
Dimensional outline (unit: mm)



Weight: Approx. 690 g (including the sensor)

KACCA0519EA

Connection example



KACCC0076EE

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- Disclaimer

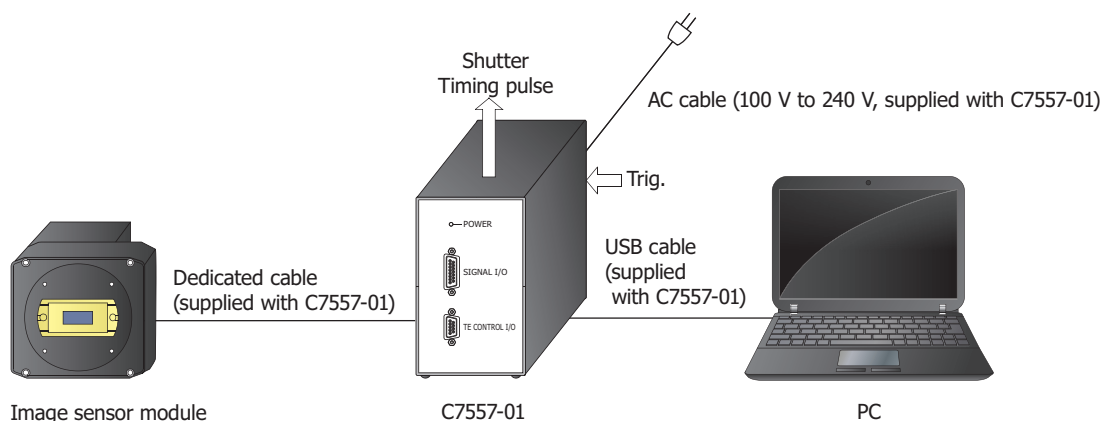
Image sensor module controller C7557-01

By connecting it to a Hamamatsu image sensor module and a PC, you can easily control the module and acquire data via the USB interface using the dedicated software.

C7557-01 accessories

- Spare fuse (2.5 A)*⁹
- AC cable
- USB cable
- Dedicated connection cable
(for the "SIGNAL I/O" and "TE CONTROL I/O" terminals of the image sensor module)
- CD-ROM (MCD USB driver, software, instruction manual)

*9: It is built in above the AC cable inlet of the C7557-01 main unit.

**Connection diagram**

Note: Shutters are not available.

KACCC0402EG

Information described in this material is current as of March 2026.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

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