

# **Photo IC for optical link**



L11354-02 S11355-04

### For 150 Mbps optical link

These photo ICs is capable of data communication at a transmission speed of 150 Mbps through a plastic optical fiber (POF). The transmitter is composed of a 650 nm RC (resonant cavity) type LED, which is suitable for POF communications, and a driver circuit that supports an LVDS interface. The transmitter has a built-in temperature compensation function that adjusts for the reduction in the light emission power caused by the high temperature of the RC type LED. This function makes light emission with stable power possible over a wide temperature range. The receiver is composed of a PIN photodiode and signal processing circuit. The adoption of a full differential structure using a dummy photodiode eliminates the effects of external noise, achieving high sensitivity. In addition, the photo IC has a sleep function that suppresses power consumption by switching to standby mode when there is no input.

These products features high quality and high reliability, allowing it to be installed even in automobiles for in-vehicle networking. It is already used as an fiber optical transceiver (FOT) for a MOST network, which is a type of in-vehicle multimedia network.

### Features

- Communication speed up to 150 Mbps
- 3.3 V power supply operation
- Wide operating temperature: -40 to +95 °C
- Suitable for reflow soldering
- ⇒ Sleep mode function

### Applications

- Data transmission in harsh, noisy environments, such as in FA and OA
- High-speed, short-distance data transmission
- Only for vehicle networks (MOST)

MOST compliant products					
Specifications of these products are subject to change without prior notice to keep up with changes in the MOST standard.					

### - Absolute maximum ratings

Para	meter	Symbol	Condition	Value	Unit
Supply voltage	L11354-02	Vcc_TX	Ta=-40 to +95 °C	-0.5 to +4.5	V
Supply voltage	S11355-04	Vcc_RX	1a40 to +95 C	-0.5 to +4.6	v
Input voltage		Vi	Ta=-40 to +95 °C	-0.5 to Vcc+0.5	V
Power dissipation L11354-02 S11355-04		P	Ta=-40 to +95 °C	350	mW
Power dissipation	S11355-04	F	1a40 to +95 C	320	IIIVV
Operating temper		Topr		-40 to +95	°C
Storage temperat	ure	Tstg		-40 to +110	°C
Soldering tempera	ature*1	Tsol		260 (3 times)	°C

<sup>\*1:</sup> Reflow soldering, JEDEC J-STD-020 MSL 2a, see P.12

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

### Recommended operating conditions

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Vcc		3.135	3.3	3.465	V
Data rate	fD	DCA coding	-	-	150	Mbps

### L11354-02

### **■** Electrical and optical characterisitics\*2 (Ta=-40 to +95 °C, Vcc=3.135 to 3.465, unless otherwise noted\*3)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Current consumption	Icc	*4	-	-	40	mA
Peak emission wavelength	λC2		635	650	675	nm
Spectral width (RMS)	σλ2		-	-	17	nm
Fiber coupled optical output 1	Po1		-7	-	-1.5	dBm
Fiber coupled optical output 2	Poff2		-	-	-50	dBm
Extinction ratio	re		10	-	-	dB
Rise time	tr	20 to 80%,*4 *5	-	-	0.5	UI
Fall time	tf	80 to 20%,*4 *5	-	-	0.5	UI
Transfer jitter (RMS)	Jtr2	*4 *5	-	-	112	ps
Overshoot	rpos	*5	See	-		
Undershoot	rnos	*5	See	"■ Undershoot" ta	ble.	-
Input voltage level \"/DCT"	Vinrl	*6	-	-	0.8	V
Input voltage level "/RST"	Vinrh	*6	2	-	-	V
Input current "/RST"	Iinrl	/RST=Low	-	-	-0.1	
input current /kS1	Iinrh	/RST=High	-	-	50	μΑ
Input current "TXN" "TXP"	Iintxl	TXN, TXP=Low	-	-	-0.1	
Input current TAN TAP	Iintxh	TXN, TXP=High	-	-	0.1	μΑ
Frequency range During transmission	FON1		12	-	73.743	MHz
During non-transmission	Foff1		0	-	10	kHz
Valid MOST data (SP2) time	ton2	*7 *8	-	-	100	μs
valid 19031 data (SP2) tillle	toff2	*7 *9	-	-	2	μs
Eye mask	A <sub>2</sub> to F <sub>2</sub>		See	e "■ Eye mask" tal	ole.	-

<sup>\*2:</sup> Electrical interface conforms to LVDS standards except common mode input voltage.

The center of the optical fiber is aligned with the center of the package lens. The distance between the fiber end and the lens top is 0.1 mm.

<sup>\*5:</sup> Input signal

Parameter	Symbol	Min.	Тур.	Max.	Unit
Differential input voltage	Vid	200	-	1272	mV
Common mode input voltage	VCM	0.05	-	Vcctx - 1.2	V
Total jitter	tTJtp1	-	-	0.15	UI
Transfer jitter (RMS)	Jtr1	-	-	50	ps

### \*6: /RST signal

Parameter		Symbol	Min.	Max.	Unit
Valid supply voltage range		VVALID	1	3.465	V
Logic switching thres		VT	2.97	-	V
Logic dolay timo	0→1	tD+	1	-	ms
Logic delay time	1→0	tD-	0	100	μs

Note: See "→Timing chart (P.5)" for the /RST signal timing chart.

<sup>\*3:</sup> Connect a bypass capacitor (0.1  $\mu$ F) between Vcc and GND at a position within 3 mm from the leads. Also connect a 10  $\mu$ F capacitor near the photo IC.

<sup>\*4:</sup> Input signal rate 150 Mbps (DCA coding)

<sup>\*7:</sup> See "►Timing chart (P.5)".

<sup>\*8:</sup> Delay time for light output to turn on

<sup>\*9:</sup> Delay time for light output to turn off

#### Overshoot

Parameter	Amplitude*10	Time (UI)	Overshoot
Ao	-0.200	-0.630	Keep out area
Во	-0.200	0.100	Jo No
Co	0.500	0.100	
Do	0.800	0.350	∫
Eo	0.800	1.370	Do Eo
Fo	0.200	-0.630	$\frac{b_0+b_1}{2}$ $\frac{H_0}{2}$ Co
Go	0.200	-0.350	Fo Go
Но	0.500	-0.100	b <sub>0</sub> =0 area
Jo	1.400	-0.100	Ao
Ко	1.400	1.370	T=0 KPICB0153EA

<sup>\*10:</sup> On the basis of  $b_0=0$  and  $b_1=1$ 

#### ■ Undershoot

■ Undershoot			
Parameter	Amplitude*11	Time (UI)*12	Undershoot
Au	0.800	-0.630 - x	
Bu	0.800	-0.530 - x	
Си	0.850	-0.530 - x	Ku Nu Pu
Du	0.850	-0.430	b <sub>1</sub> =1
Eu	0.800	-0.430	CuDu Keep out area
Fu	0.800	-0.350	Au T Bu Eu Tru
Gυ	0.500	-0.100	
Hυ	-0.200	-0.100	$\frac{b_0 + b_1}{2} \overline{GU} - RU$
Ju	-0.200	1.370	] Su Tu
Ku	1.400	-0.630 - x	
Lu	1.400	-0.530 - x	
Mυ	1.340	-0.530 - x	
Nυ	1.150	-0.220 - x	
Pu	1.150	0.100	Hu Hu → Your - who are
Ru	0.500	0.100	Keep out area
Su	0.200	0.350	T=0
Τυ	0.200	1.370	KPICB0154EB

<sup>\*11:</sup> On the basis of  $b_0=0$  and  $b_1=1$ 

For 2UI: x=0, For 6UI: x=4

### ■ Eye mask

Parameter	Amplitude	Time (UI)	Eye mask
A2	$0.5 \times (b_1 + b_0)$	0.150	
B2	0.8 × (b1 - b0)+b0	0.400	B <sub>2</sub> C <sub>2</sub>
C2	0.8 × (b1 - b0)+b0	0.600	Az Keen out area Dz
D2	$0.5 \times (b_1 + b_0)$	0.850	A2 Keep out area D2
<b>E</b> 2	0.2 × (b1 - b0)+b0	0.600	F <sub>2</sub> E <sub>2</sub>
F2	0.2 × (b1 - b0)+b0	0.400	KPICB0155EA



<sup>\*12:</sup> The positions of Au, Bu, Cu, Ku, Lu and Mu on the time axis depend on the MOST data pulse width (2 to 6). x=MOST data pulse width - 2

### S11355-04

### **=** Electrical and optical characterisitics\*13 (Ta=-40 to +95 °C, Vcc=3.135 to 3.465, unless otherwise noted\*14)

Parameter		Symbol	Condition	Min.	Тур.	Max.	Unit
Peak sensitivity wavelength		λр		-	800	-	nm
Current co	nsumption (operation mode)	Icco	*15	-	-	45	mA
Current co	nsumption (sleeping mode)	Iccs	Dark state	-	-	30	μA
STATUS	High level output voltage	Vmh	Imh=20 μA* <sup>16</sup>	2.5	-	-	V
SIAIUS	Low level output voltage	Vml	Iml=0.88 mA (S11355-04)*16	-	-	0.5	V
Operation to sleeping mode transition receivable level		Poff3	*15 *17 *18	-	-	-35	dBm
Transfer jit	tter (RMS)	Jtr4		-	-	230	ps
Error rate		Pe	*15 *18 *19 *20 *21	-	-	10 <sup>-9</sup>	-
Valid MOS	T data (input signal) frequency	Fon3		12	-	73.743	MHz
Invalid MO	OST data frequency	Foff3		0	-	10	kHz
Time from in	nput signal start to operation mode	tON4	*22	-	-	10	ms
Time from	input signal start to STATUS ON	tStatf	*22	200	-	1000	μs
Time from STATUS ON to LVDS output stabilization		tlvdsv4	*22	-	-	100	μs
Time from input signal stop to sleeping mode		toff4	*22	-	-	1	ms
LVDS output hold time		tlvdsh4	*22	1	-	-	μs
Time from	input signal stop to STATUS OFF	tstatr	*22	-	-	2	μs
Eye mask		A4 to F4		See	"■Eye mask" ta	able.	-

- \*13: Electrical interface conforms to LVDS standards except differential output amplitude level
- \*14: Connect a bypass capacitor (0.1  $\mu$ F) between Vcc and GND at a position within 3 mm from the leads. Also connect a 10  $\mu$ F capacitor near the photo IC.
- \*15: Input signal

Parameter	Symbol	Min.	Тур.	Max.	Unit
Optical data that allows Valid MOST data (SP4) to be obtained	Popt3	-23.5	-	-2	dBm
Rise time	trtp3	-	-	2	ns
Fall time	tftp3	-	-	2	ns
Total jitter	tTJtp3	-	-	600	ps
Extinction ratio	re	10	-	-	dB

- \*16: Changes to operation mode when input light enters the receiver section. When STATUS output is low, the photo IC is in operation mode. When STATUS output is high, the photo IC is in sleeping mode.
- \*17: Input light is specified as the average power at the fiber end. The optical fiber used is a POF (NA=0.5).
- \*18: The center of the optical fiber is aligned with the center of the package lens. The distance between the fiber end and the lens top is 0.1 mm.
- \*19: RL=100 kΩ, CL=3 pF (including parasitic capacitance such as probe, connector and circuit board)
- \*20: A standard transmitter specified by HAMAMATSU is used to input light.
- \*21: Optical input signal rate=150 Mbps (DCA coding)
- \*22: See "-Timing chart (P.5)".

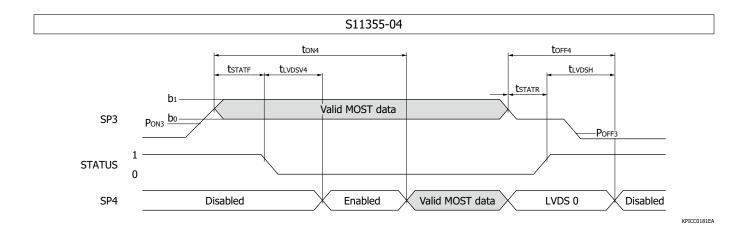
### ■ Eye mask

Parameter	Amplitude (mV)	Time (UI)	Eye mask
A4	0	0.275	G <sub>4</sub> Keep out area
B4	148	0.425	
C4	148	0.575	B4 C4
D4	0	0.725	A4 Keep out area D4
E4	-148	0.575	Neep out area
F4	-148	0.425	F4 E4
G4	636	-	
H4	-636	-	H4 Keep out area KPICB0156EA

### - Timing chart

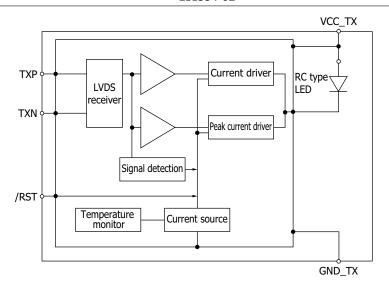
#### L11354-02 Signal: ON Reset: ON VCCTXOR max. VCCTXOR min. VCCTXOR max. VCCTXOR min. EOC power EOC power 0 V 0 V t<sub>D+</sub> /RST /RST Valid MOST data SP1 Disabled LVDS 0 Valid MOST data SP1 Disabled $b_1$ Valid MOST data Valid MOST data SP2 SP2 ton2 ton2 Signal: OFF Reset: OFF EOC power EOC power $V_{\mathsf{T}}$ 0 V 0 V t<sub>D</sub>-/RST /RST SP1 Valid MOST data Valid MOST data LVDS 0 Disabled Disabled SP1 SP2 Valid MOST data Valid MOST data **b**<sub>0</sub> · **b**<sub>0</sub> Poff2 toff2 toff2

KPICC0176EA



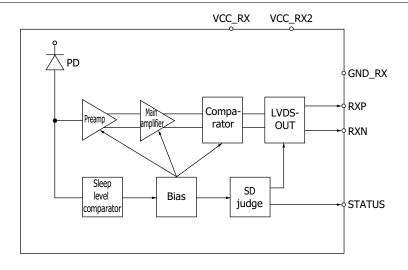
### **Block diagram**

### L11354-02



KPICC0177EC

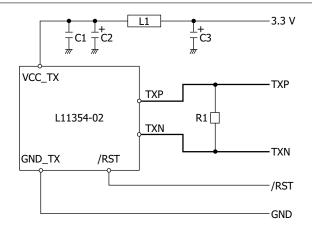
### S11355-04



KPICC0178EA

### - Connection example

### L11354-02

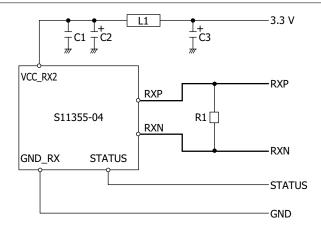


\*Bold line: 50  $\Omega$  impedance matching

KPICC0179EB

Symbol	Part	Constant
R1	Resistor	100 Ω
L1	Inductance	0.1 μΗ
C1	Capacitor	0.1 μF
C2	Capacitor	10 μF
C3	Capacitor	10 μF

### S11355-04

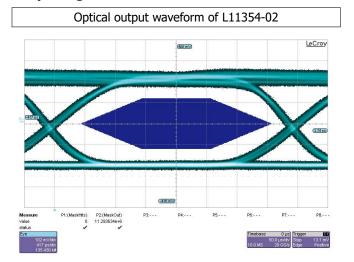


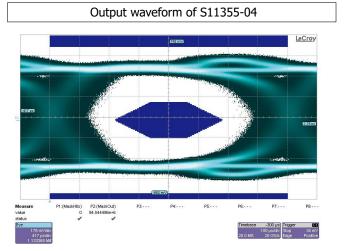
\*Bold line: 50  $\Omega$  impedance matching

KPICC0180EE

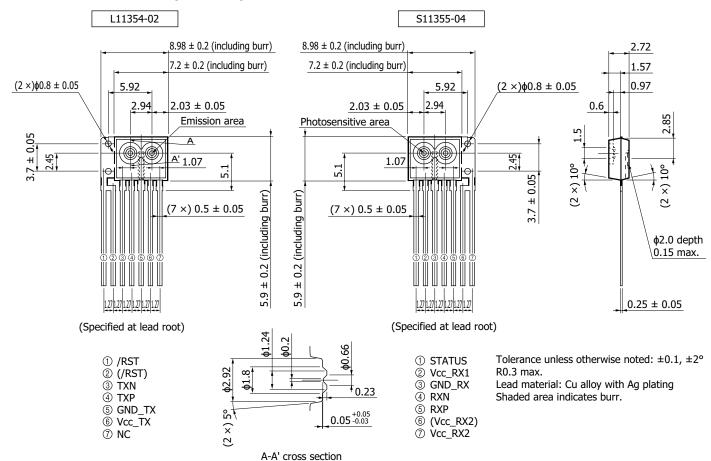
Symbol	Part	Constant
R1	Resistor	100 Ω
L1	Inductance	0.1 μΗ
C1	Capacitor	0.1 μF
C2	Capacitor	10 μF
C3	Capacitor	10 μF

### **Eye diagram**



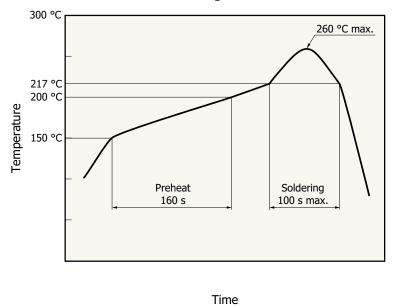


### - Dimensional outlines (unit: mm)



KPICA0079ED

### Recommended reflow soldering conditions



• This product supports lead-free soldering. After unpacking, store it in an environment at a temperature of 30 °C or less and a humidity of 60% or less, and perform soldering within 4 weeks.

KPICB0168EB

• The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. Before actual reflow soldering, check for any problems by tesiting out the reflow soldering methods in advance.

#### Related information

www.hamamatsu.com/sp/ssd/doc\_en.html

- Precautions
- Disclaimer
- · Metal, ceramic, plastic products

Information described in this material is current as of July 2024.

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## **HAMAMATSU**

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