

The S15534 is a surface mount type two-dimensional PSD with excellent position detection characteristics. It is smaller than the conventional S5990-01.

Features

COB type

Excellent position detectability

Small package: 7.21 × 5.96 × 1.5^t mm

Compatible with lead-free solder reflow

Applications

Light spot detection

Pointing device

Various types of position detection

Options (sold separately)

Signal processing circuit for 2-D PSD

C4674-01

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Structure

Parameter	Symbol	Specification	Unit
Photosensitive area	A	4 × 4	mm
Package	-	Glass epoxy	-
Window material	-	Silicone resin	-

Absolute maximum ratings (Ta=25 °C)

Parameter	Symbol	Value	Unit
Reverse voltage	VR max	20	V
Operating temperature*1	Topr	-20 to +60	°C
Storage temperature*1	Tstg	-20 to +80	°C
Soldering temperature Tsol		260 (3 times)*2	°C

*1: No dew condensation. When there is a temperature difference between a product and the surrounding area in high humidity environments, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

*2: Reflow soldering, JEDEC J-STD-020 MSL 3, see P.5

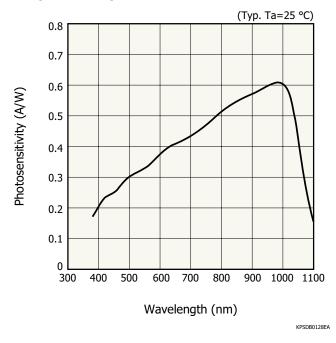
Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

2-	Electrical	and	optical	characteristics	(Ta=25 °	C)
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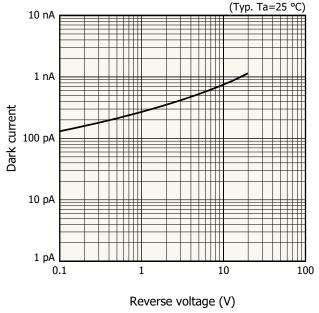
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Spectral response range	λ		-	380 to 1100	-	nm
Peak sensitivity wavelength	λр		-	980	-	nm
Photosensitivity	S	λ=λp	-	0.6	-	A/W
Interelectrode resistance	Rie	Vb=0.1 V	5	7	15	kΩ
Position detection error	E	λ =900 nm, VR=5 V light spot: ϕ 0.2 mm ^{*3}	-	±70	±150	μm
Saturation photocurrent	Ist	λ=900 nm, VR=5 V RL=1 kΩ	-	500	-	μA
Dark current	ID	VR=5 V	-	0.5	10	nA
Rise time	tr	VR=5 V, RL=1 kΩ λ=900 nm	-	1	-	μs
Terminal capacitance	Ct	VR=5 V, f=10 kHz	-	70	-	pF
Position resolution	ΔR	Io=1 µA, B=1 kHz*3	-	0.7	-	μm

*3: Specified within a circle that is 80% of the photosensitive area. Recommended light spot size: ϕ 0.2 mm or more

Spectral response



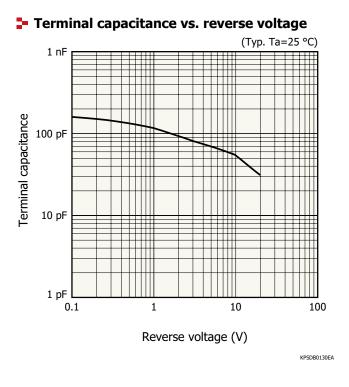
Dark current vs. reverse voltage



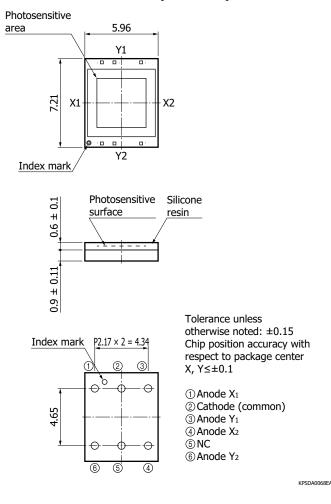
KPSDB0129EA



Two-dimensional PSD



Dimensional outline (unit: mm)



Conversion formula of spot light position on the PSD

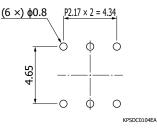
Output signals (photocurrent) IX1, IX2, IY1, IY2 obtained from electrodes and the light spot position x, y can be found by the following formula.

 $\frac{(Ix2 + Iy1) - (Ix1 + Iy2)}{Ix1 + Ix2 + Iy1 + Iy2} = \frac{2x}{Lx}$ $\frac{(Ix2 + Iy2) - (Ix1 + Iy1)}{Ix1 + Ix2 + Iy1 + Iy2} = \frac{2y}{Ly}$

Ix1 : Output signal from electrode X1

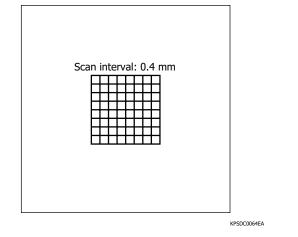
- Ix2 : Output signal from electrode X2
- IY1 : Output signal from electrode Y1
- IY2 : Output signal from electrode Y2
- x, y : Position coordinate of light spot
- Lx, Ly: Resistance length (4.5 mm)

Recommended land pattern (unit: mm)





Example of position detectability (Ta=25 °C, λ=900 nm, light spot size: φ0.2 mm)

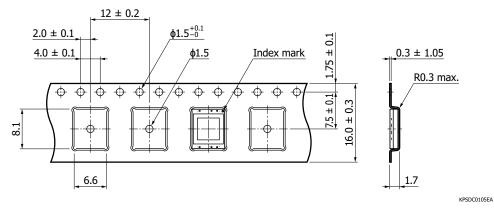


Standard packing specifications

Reel (conforms to JEITA ET-7200)

Outer diameter	Hub diameter	Tape width	Material	Electrostatic characteristics
ф330 mm	φ100 mm	16 mm	PS	Conductive

Embossed tape (unit: mm, material: PS, conductive)



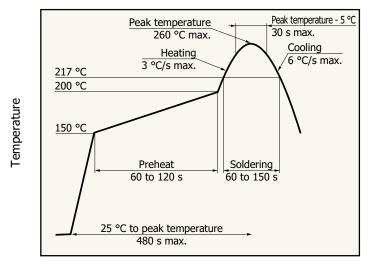
- Packing quantity 500 pcs/reel
- Packing state

Reel and desiccant in moisture-proof packaging (vacuum-sealed)



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Recommended reflow soldering conditions



Time

· After unpacking, store in an environment at a temperature of 5 to 30 °C and a humidity 60% or less, and perform reflow soldering within 168 hours.

The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. When you set reflow soldering conditions, check that problems do not occur in the product by testing out the conditions in advance.

Baking

If more than 12 months have passed in the unopened state, or storage conditions are exceeded after opening the package, baking is required to remove moisture before reflow soldering. For the baking, refer to "Precautions / Surface mount type products" in the related information.

KSPDB0419EA

Recommended baking conditions

Temperature: 120 °C, 3 hours, up to twice Note: Before setting the baking conditions, perform experiments to confirm that no problems occur with the product.

Related information

www.hamamatsu.com/sp/ssd/doc_en.html

- Precautions
- Disclaimer
- · Precautions / Surface mount type products

Catalogs

· Technical note / PSD

Information described in this material is current as of January 2025.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

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